

Notice of References Cited	Application/Control No. 10/637,664		Applicant(s)/Patent Under Reexamination IZUMI ET AL.	
	Examiner William M. Brewster		Art Unit 2823	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
X	A	US-6,076,933 A	06-2000	DiLoreto et al.	359/614
X	B	US-5,670,384	09-1997	Needham, Christopher R.	438/70
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	10-229180	08-1998	Japan	Kuroiwa Atsushi	H01L 27-14
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf Ph.D. in Silicon Processing for the VLSI Era, Volume 2: Process Integration, Lattice Press, 1990, pp. 229-232.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.